

## STUDENT GUIDANCE - PH.D.

### Ph.D. Scholars Details -Completed

Sl. No	Name of Student (Reg. No)	Category	Year of registration/ completion	Title of the Thesis	Area of specialization
1	G. Vivek (P120102EE)	Full Time	Registration: 2012 Completion: 2019	Design Space Exploration of Hybrid Advanced Bus Clamping SVPWM Strategies for Multilevel Inverter	Power Electronics
2	Anirudh K. M. (P120025EE)	Full Time	Registration:2012 Completion: 2018	Design, Architecture and Real Time Distributed Maximum Power Point Tracking (DMPPT) method for PV Systems	Power Electronics
3	Meenu D. Nair (P110099EE)	Full Time	Registration: 2011 Completion: 2018	Design Space Exploration in Space Vector Pulse Width Modulation (SVPWM) Techniques	Power Electronics

### Ph.D. Scholars Details -Ongoing

Sl. No	Name of Student (Reg. No)	Category	Year of registration	Title of the Thesis	Area of specialization
1	Anjana K. G. (P140029EE)	Full Time	2014	Design and Development of Multilevel PV Inverter with SVPWM Digital Controller Architecture for grid connected PV systems	Power Electronics

2	M. Aswini Kumar (P140071EE)	Full Time	2014	Design and Development of modular multilevel inverter with SVPWM Digital Controller Architecture for medium voltage applications	Power Electronics
3	Nithin S. Nair (P180077EE)	Full Time	2018	Modular multilevel converter	Power Electronics

### M. Tech Projects -Completed

Sl. No	Name of Student (Reg. No)	Category	Year of completion	Title of the Thesis
1	Abhishek Roy (M170151EE)	Full Time	2018-2019	Study, Design and Implementation of Soft Switching PWM Dc-Dc Buck Converter
2	Arundhati Parida (M170145EE)	Full Time	2018-2019	Design and Implementation of An Isolated Bidirectional Dc-Dc Converter for Electric Vehicles
3	Partha Sarathi Behera (M160092EE)	Full Time	2017-2018	Study of Common Mode Voltage (CMV) In Three Level NPC VSI Using Advanced SVPWM Methods
4	Abir De (M150315EE)	Full Time	2016-17	An Improved ZVS-SVPWM Technique with Voltage Mode Digital Control For Three Phase Inverter

5	Janjanam Naveen (M150288EE)	Full Time	2016-17	Modeling, Design and Implementation of An Improved Buck-Boost Converter with Voltage Mode Control for Low Power Applications
6	Shubham Goyal (M150262EE)	Full Time	2016-17	Modeling, Design and implementation of high gain Boost converter with voltage-Mode control
7	Betsy Maria Mathews (M140187EE)	Full Time	2015-16	Multi-Port DC-DC Converter with Simultaneous Buck and Boost Outputs
8	Rekha Sree Korlipara (M140560EE)	Full Time	2015-16	Design and Implementation of ZVS Bidirectional DC-DC Converter
9	Remya M. N. (M140238EE)	Full Time	2015-16	PDM Controlled Half-Bridge Series Resonant Inverter for Induction Heating Application
10	Tatireddy Devarajulu Reddy (M130188EE)	Full Time	2014-15	Design and Implementation of An Active Filter To Eliminate Dominant Harmonics from A Square-Wave Inverter
11	Kukkala Satya Prakash (M130336EE)	Full Time	2014-15	Design and Implementation of Closed Loop Control of Dc-Dc Boost Converter
12	Satender (M130247EE)	Full Time	2014-15	Design and Implementation of PFC AC-DC Boost Converter
13	Jini P. G. (M120444EE)	Full Time	2013-14	Design, Development and Control of Single Phase PWM Boost Rectifier

14	M. Aswini Kumar (M120199EE)	Full Time	2013-14	Design and Implementation of Single Phase and Three Phase Z-Source Inverter
15	Sithara M. (M120141EE)	Full Time	2013-14	Design and Implementation of An Efficient AC-DC Converter for Low Voltage Applications
16	Mohammed Ajlif A. (M120081EE)	Full Time	2013-14	Design and Implementation of a Half Bridge Boost Rectifier
17	Anjana K. G. (M110459EE)	Full Time	2012-13	Design and Development of Three Phase Two Level Inverter Using Bus Clamped SVPWM Techniques
18	Kalirathnam A. (M090206EE)	Full Time	2011-12	Performance Study of Sine PWM And Third Harmonic PWM Methods for Single and Three Phase Inverter
19	Poothi J N Ratna Bhavani (M100471EE)	Full Time	2011-12	Performance Study of a Single Phase 5-Level
20	Ganji Srinivasa Rao (M100473EE)	Full Time	2011-12	Design and Implementation of Three Phase 3-Level NPC Diode Clamped MLI Using Carrier Based Sine PWM And Multilevel Charge Pump Technique
21	Neetu Mohan (M100120EE)	Full Time	2011-12	Design and Implementation of Zero Voltage Switching Active Clamping DC-DC Converter
22	Nigel Joseph (M090028EE)	Full Time	2010-11	Study, Design & Development Of 3-Level Flying Capacitor Multilevel Inverter Using Sine PWM (SPWM) Technique

23	Satya Venkata Kishore P. (M090186EE)	Full Time	2010-11	Study, Design and Development Of 5-Level Diode Clamped MLI Using Space Vector PWM (SVPWM) Technique
24	S. Athaullah (M090199EE)	Full Time	2010-11	Study, Design and Development Of 5-Level Cascaded H-Bridge MLI Using Phase Disposition PWM(PDPWM) Technique

### **M. Tech Projects -Ongoing**

<b>Sl. No</b>	<b>Name of Student (Reg. No)</b>	<b>Category</b>	<b>Year of registration</b>	<b>Title of the Thesis</b>
1	Kumar Raja M. M180148EE	Full Time	2018	Design and Implementation of Integrated Dual Output L-Z Source Inverter for Hybrid Electric Vehicle
2	Konagalla Ram Kiran M160457EE	Part Time	2018	Design and Implementation of A Bi-Directional DC-DC Converter