

ABOUT NIT CALICUT

The National Institute of Technology Calicut (NITC) is a centrally funded institute under the MoE, governed by the NIT Act, 2007. It comprises fifteen departments, and eighteen research centers, offering thirteen undergraduate, thirty three postgraduate, and Ph.D programmes in various fields of Science, Technology, Education and Engineering. The faculty actively collaborates with leading institutions in India and abroad and engages in industry consultancy. For more details, visit: www.nitc.ac.in

ABOUT CALICUT

NIT Calicut (NITC) is located 22 km northeast of Calicut (Kozhikode), a major knowledge hub in Kerala and home to institutions like NITC, IIMK, and CWRDM. The city is well-connected by road, rail, and air to major Indian cities. Notable attractions include Kappad Beach, Kozhikode Beach, and Tusharagiri Waterfalls.



ABOUT THE DEPARTMENT

The Electronics & Communication Engineering (ECE) Department at NIT Calicut began as part of the Electrical Engineering Department in 1980. In response to rapid advancements in the field, it became a separate department in 1997.

Key Highlights

- Offers 4 M.Tech programmes:
 - Electronic Design Technology
 - Microelectronics & VLSI
 - Signal Processing
 - Telecommunication
- Recognized AICTE QIP Centre for both M.Tech and Ph.D.
- Active R&D and sponsored research by faculty
- Strong emphasis on doctoral research in diverse ECE domains

For more information: www.ece.nitc.ac.in

Five Day Short Term Training Programme (STTP) on

**CUSTOM IC DESIGN
AND LAYOUT USING
Standard 180nm PDK**

30th June – 4th July, 2025

ORGANIZED BY
DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING



NATIONAL INSTITUTE OF
TECHNOLOGY, CALICUT

IN ASSOCIATION WITH



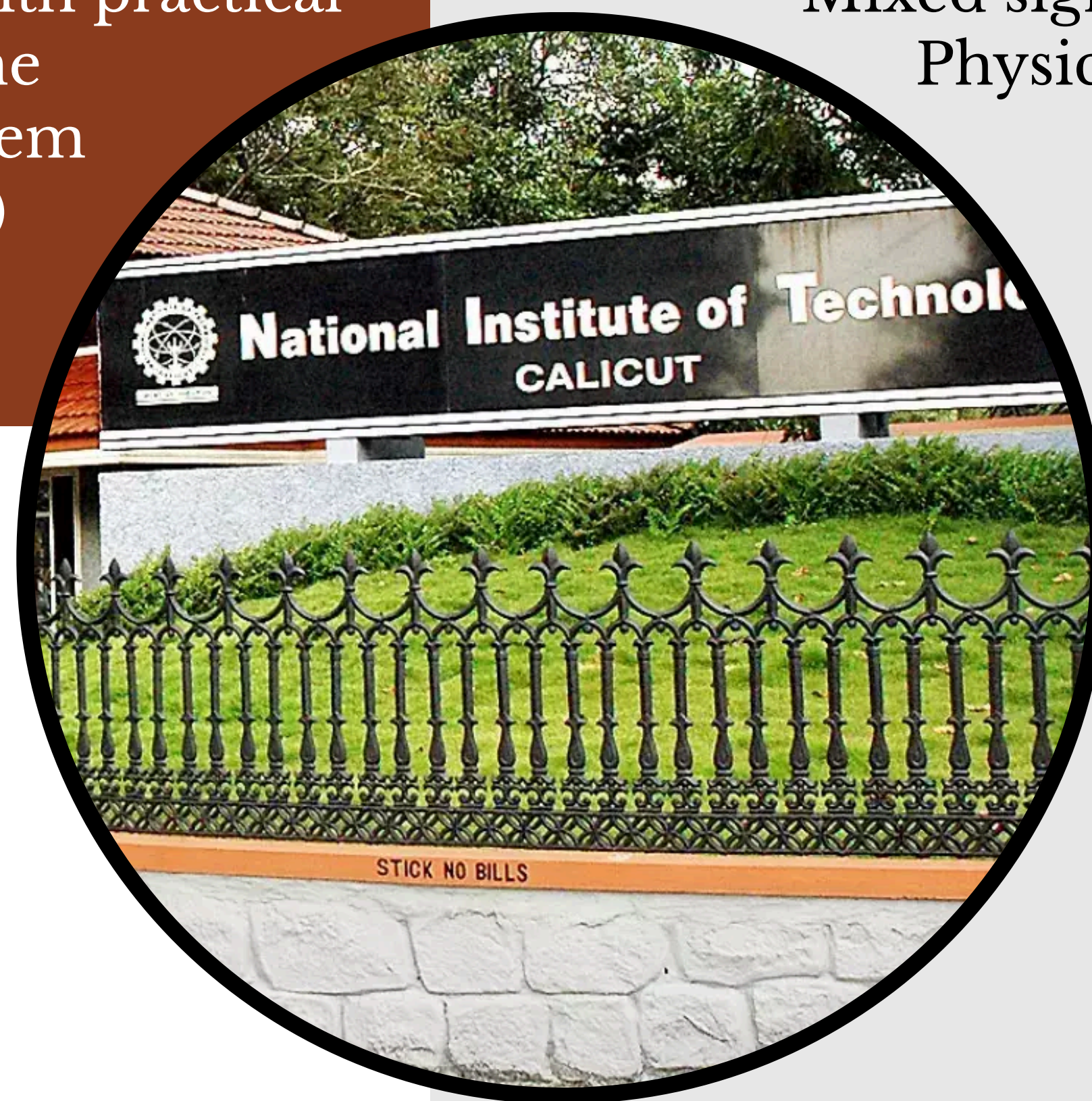
PREAMBLE

In light of the growing momentum around India's Semiconductor Mission and the expanding VLSI landscape through Chips to Startup (C2S) programme, our institute is offering a Short-Term Training Programme (STTP) on "Custom IC Design using 180nm PDK." The course provides fundamentals of Custom IC Design and hands-on experience with industry-standard EDA tools, covering schematic capture, layout design, simulations, and physical verification. This initiative aims to equip students and professionals with practical skills that align with the needs of the country's evolving Electronics System Design and Manufacturing (ESDM) ecosystem.

WHAT YOU'LL LEARN

By the end of this course, participants will be able to:

- Describe the principles and practical aspects of Custom IC Design.
- Use the EDA tools to design using standard 180nm PDK
- Perform the Schematic Capture and Pre-Layout simulations of the design under consideration using EDA tools
- Perform the Custom Layout Design and Post Layout Simulations of the design under consideration using EDA tools
- Implement the I/O pad ring interconnection with the core circuit, perform Physical Verification and obtain the GDSII file making the design ready for fabrication



WHO CAN APPLY

This course is designed for:

- Undergraduate and graduate students and research scholars in Electronics and Communication Engineering, Electrical Engineering, Computer Science, related fields.
- Faculty members interested in Digital/ Analog/Mixed signal IC Design and Physical Design.
- Industry professionals and R&D engineers working in Digital/Analog/ Mixed signal IC Design and Physical Design.

HOW TO APPLY?

For registration, fill the online form at:

<https://forms.gle/zXQxap2DRLA3Zg3k7>

Last date for registration is 20th June 2025

Selection Criteria: Selection will be done based on first-come-first-serve basis and the academic/professional credentials provided in the candidate's biodata. Selected candidates need to pay registration fee (Details given below).Certificates will be provided to registered participants upon completion of the course.

REGISTRATION FEE DETAILS

- Registration Fee for (Non NITC Faculty): Rs.4200
- Registration Fee (NITC Faculty): Rs.2800
- Registration Fee for (Personnel from Industries/ R&D Organizations): Rs.8300
- Registration Fee (Non NITC students): Rs.2100
- Registration Fee (Non NITC IEEE SSCS students): Rs.1600
- Registration Fee (NITC students): Rs.1000

RESOURCE PERSONS

All the lecture sessions will be handled by faculty of NITC and invited experts.

Confirmed invited speakers are

Dr.Gajendranath Chaudhury, IIT Hyderabad
Prof. Srihari Rao Patri, NIT Warangal
Dr. Kapil Jainwal, IIT Hyderabad
Dr Suresh P R, Exiger Technologies Pvt Ltd

CO-ORDINATORS

Dr. Bhuvan B. & Dr. Dhanaraj K. J.
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FOR REGISTRATION AND
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