NATIONAL INSTITUTE OF TECHNOLOGY CALICUT
<u>Offline Interview</u>

An Offline Interview for the engagement of a **Junior Research Fellow (on contract basis)** for a Research Project in the Department of Electronics and Communication Engineering, NIT Calicut is scheduled to be held on **5th March 2024** in the **Department of Electronics and Communication Engineering**, National Institute of Technology Calicut- 673601.

No. of Positions	:	**One (1), Junior Research Fellow (JRF)**
Duration of Engagement	:	**1 Year** (on contract basis) and extendable based on the performance of the candidate and availability of the project fund
Name of the sponsoring Agency	:	DST Inspire Faculty Scheme
Name of Project	:	"Exploring the Use of Index Coding in Coded Caching Problems and Vice- Versa"
Qualifications	:	1) M.Tech/ M.E in Electronics and Communication/ Signal Processing or closely related areas or Graduate or Post Graduate degree in (Electronics and Communication) and M.E/ M.Tech with first class (marks not below 60% or with CGPA not below 6.5). 2) Qualified in National eligibility tests/ National Level Examination conducted by NIT Calicut
Desirable qualification	:	M.Tech in Telecommunication/ Communication and Networks or Signal Processing.
Desirable Skills	:	1) MATLAB coding 2) Background in linear algebra and information theory and coding
Consolidated monthly pay	:	Rs.37,000/- + 18% HRA per month (Consolidated)
Other Benefits	:	Accommodation may be available as per the availability and the institution norms. Facility of yearly leave may be available as per the applicable rules for the project staff.
Last date for application submission	:	**3rd March 2024**
Tentative date for Interview	:	**5th March 2024**

* The selected candidates are encouraged to register for a Ph.D. at NIT Calicut.

<u>Note:</u>

1) Candidates have to send an application form in the prescribed format (annexed) as e-mail to hodeced@nitc.ac.in with subject line "Application for JRF: DST-INSPIRE" and also with a copy to nujoom@nitc.ac.in. The self-attested copy of all the relevant certificates/document proofs scanned and merged as a single PDF file also has to be sent along with the application form.

   The applicant will be responsible for the authenticity of information, other documents and photographs submitted.

2) The selection process is based on a written test and a technical interview. Candidates, satisfying the minimum eligibility conditions will be required to walk-in to attend the selection process as per the schedule given below along with a most recent CV and original mark sheets/ certificates in support of their academic qualifications/ experience:

Reporting date and time   :   5th March 2024 at 9.00 AM

Venue   :   Department of Electronics and Communication Engineering, National Institute of Technology Calicut

3) After shortlisting, the eligible candidates will be invited for the interview.

4) The Institute reserves the right to relax authenticity of information, other documents and photographs submitted.

5) Applicants who are employed in Government or any other organizations are required to send a "No Objection Certificate" from the employer, if selected for the interview.

6) For any queries contact Dr.Nujoom Sageer Karat, E.mail ID: nujoom@nitc.ac.in

DEAN(R&C)