

Department of Computer Science and Engineering
National Institute of Technology Calicut

NIT Campus (PO), Calicut-673601, India

DCC Meeting Minutes

Date: 09/02/2023 **Time:** 03:00 PM to 04:00 PM **Venue/Mode:** CSED Seminar Hall/Offline

Agenda Items:

1. **Ratification of the minutes of the DCC meeting held on 18-01-2023**
2. **Action Taken Action Pending Report of the last meeting**
3. **Issues and challenges of the TT with the New Slot System**
4. **Preparation for M Tech CS-IS accreditation**
5. **Curriculum revision of both UG & PG**

The DCC meeting started at CSED Seminar hall at 03:00 PM. The Chairperson welcomed all members to the meeting.

Agenda Item 1: Ratification of the minutes of the DCC meeting held on 18-01-2023

The DCC ratified the confirmation of the minutes of the DCC meeting dated 18-01-2023

Agenda Item 2: Action Taken Action Pending Report.

Action items in the DCC meeting dated **18/01/2023**

Item	Status
- NIL -	

Agenda Item 3: Issues and challenges of the TT with the New Slot System

Dr Vasudevan A R (member, CSED TT) presented the following observations/suggestions made by the time table committee of CSED regarding the issues and challenges of the time table with the new slot system.

- a) Upper limit for UG/PG elective courses
CSED TT: An upper limit on the number of students for the elective courses to ensure that all the elective courses offered by the department get the minimum number of students required to run the course as decided in an earlier DCC meeting.

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DCC: DCC agreed in principle to have an upper limit of 50 students for UG elective courses and upper limit of 30 students for PG elective courses.

b) Dwindling number of Full-Time Ph.D. Research Scholars for TAship

CSED TT: CSED TT observed a trend that of late the majority of the research scholars joining the department are under the "external" category. If this trend continues, it can lead to shortage of TA's for labs as the department can not make use of the expertise of the research scholars under "external" category for lab activities.

DCC: DCC concurred with the view of the time table committee and DCC suggested the department take up widespread advertisement of PhD programmes offered by the department to attract the students to join under institute fellowships.

c) Non-compliance to TA'ship

CSED TT: There is a widespread concern from faculty members about non-reporting/unprepared to TA lab duty assigned to PG students and Ph.D. scholars/ and assistance-ship to faculty.

- M.Tech. students and regular Ph.D. scholars may be alerted by the concerned faculty, then Program Coordinator and HoD (in that order) about reporting to lab duty, without fail.
- Each M.Tech. student is associated with a faculty member. The monthly report of the student may also bear a signature of his/her associated faculty member. In case of any misreporting by the student the Program Coordinator and the HoD may be alerted.

DCC: DCC recommended having a separate entry for the signature of the faculty member associated with a TA for the theory course also in the monthly reports of the students.

d) Overlapping PG course contents

CSED-TT: It was notified by the faculty members that some of the elective courses offered to the PG students have upto 50% overlapping course contents. CSED-TT may ensure that overlapping courses (specified in the syllabus) get offered in the same slots so that students do not credit the overlapping subjects. The students are also advised to register for the courses with non-overlapping syllabi. Also, in the revised/new curriculum (that is in the offing), each faculty member who frames the syllabus may be requested to check for overlaps with other courses.

E) Third PG programme on DS & AI

CSED-TT: The third M.Tech. programme on AI & DS may be given a head start as early as possible so as to accommodate more faculty members in the core/elective courses.

F) UG Curriculum Revision

CSED-TT: Some suggestions for UG curriculum revision

- Some of the existing core courses may be split into two (e.g. Discrete Structures)

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- Some of the existing elective courses may be categorized as core courses (e.g. Design and Analysis of Algorithms)
- More number of laboratory elective courses may be added to the curriculum and floated by the faculty members; faculty research group may include skill-set specific lab courses in the higher semesters

Agenda Item 4: Preparation for M Tech CS-IS accreditation

HoD requested all the faculties and staff for their full support and cooperation in the preparation for MTech CS-IS accreditation and for the internal academic audit of M Tech CS & MTech CS-IS.

Agenda Item 5: Curriculum revision of both UG & PG

HoD presented the proposal for formation of committees(Annexure I) for UG curriculum revision and PG curriculum revision and DCC approved the proposal.

Meeting ended at 4:00 PM on 09-02-2023.

Annexure I

PG curriculum revision committee:

- 1) Dr. Abdul Nazeer K A
- 2) Dr. Madhu Kumar S D
- 3) Dr. Priya Chandran (Coordinator)**

UG curriculum revision committee:

- 1) Dr. Murali Krishnan K
- 2) Dr. Nirmal Kumar Boran
- 3) Dr. Sheerazuddin S
- 4) Dr. Vinod P (Coordinator).**

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